



Seamless Update Whitley ICX LCC - Wilson City Reference Platform

BKC Release WW18 '20

Seamless Update Whitley ICX-LCC – BKC Release

Intel announces the release of the Seamless Update Best Known Configuration (BKC) package. This release was tested on the Wilson City CRB running Fedora operating system on the Ice Lake LCC processor.

The BKC consists of an update package and associated report, published on a regular basis, for the Wilson City reference platform. The BKC package includes UEFI Firmware and Management Engine Firmware (SPS). BKC testing is designed to verify the interoperability of the various firmware and driver components which have already undergone extensive individual validation.

By downloading this BKC package, customers can be confident that they have the very latest revisions of all reference platform firmware and software, and that those components work together.

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- BIOS capsule upgrade test from D31 to D32 and downgrade test from D32 to D31 have been verified in this release.
- uCode capsule upgrade test from uCode 0x30 to 0x31 and downgrade test from uCode 0x31 to 0x30 have been verified in this release.
- SPS capsule upgrade from SPS 0x29 to SPS 0x44, and downgrade from SPS 0x44 to SPS 0x21 have been verified in this release.
- SPS upgrade and downgrade test have been covered for both single capsule and dual capsule method.
- OOB update of SPS, Microcode and BIOS seamless update flows have been verified in this release.
- It's a known issue that to verify OOB updates on Linux need to disable the TPM(refer instructions below)

TPM disable steps

- Run the following commands on Linux shell:
 1. **ls /sys/bus/platform/drivers/tpm_tis** # find the device name in "xxxxxxx:xx" format, such like "IFX0762:00"
 2. **echo IFX0762:00 > /sys/bus/platform/drivers/tpm_tis/unbind** #use the device name you see to replace IFX0762:00

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New Sightings*

*Sightings found during BKC validation

ID	Title	Status
1507648647	[Microcode Update] IFWI hang with PEI_ASSERT after OOB microcode update or BIOS update in Linux	Under Investigation
18011069381	Sending various commands to ME during seamless update make ME be in RCV after seamless update after switch to BMC	Under Investigation
14011394345	Seamless update proceeds with corrupted update capsule	Under Investigation
14011432506	BSP FatalError at POST - Major Code 0x3D, Minor Code 0x1	Under Investigation
1507887926	[BOOT] Boot to Fedora/windows OS fail with ICX debug image	Under Investigation
1507952161	[Seamless][Memory] Can Not change Reserve Memory Size	Under Investigation
14011354702	PMEM estimated quiesce time value in BIOS is incorrect	Planned Fix

Parameter	Definition
Planned fix	This known issue may be fixed in a future BKC release.
Fixed in XX BKC	The issue is fixed on XX BKC.
Under Investigation	The sighting is under investigation. It may not be a defect.

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Existing Sightings*

*Sightings found during BKC validation

ID	Title	Status
14011155319	Patch uCode fails after a latebinding uCode update	Under Investigation
1507815978	Disable "Serial Debug Message Level" could NOT work, still have serial logs print	Under Investigation
1507841182	[uCode Update OOB] SUT boot FAIL when microcode 0x2c and 0x2d in the same IFWI after ucode update	Under Investigation
14010832509	Platform hangs OOB loop memory issue leads to BMC kernel panic	Under Investigation
2209881377	OpenBMC Does not issue Warm Reset on Seamless BIOS Update	Under Investigation

Parameter	Definition
Planned fix	This known issue may be fixed in a future BKC release.
Fixed in XX BKC	The issue is fixed on XX BKC.
Under Investigation	The sighting is under investigation. It may not be a defect.

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Closed Sightings*

*Sightings found during BKC validation

ID	Title	Status
14011135582	BMC responds with HTTP 202 and content after using update service	Under Investigation
16010804221	BMC_RemoteControl_PowerOnOff Through WEB_O Override System Boot to Menu Not Working	Under Investigation
1507854929	PCH SPI handshake between BIOS and BMC doesn't work	Under Investigation
1507871657	[Microcode Update] The microcode revision in SMBIOS is not consistent with the microcode revision in used	Planned Fix
16010945788	SUT not entering to BIOS after BMC_RemoteControl_Boot_BIOS	Under Investigation
22010271035	Invalid seamless update capsule signing does not generate an EventLog entry	Under Investigation
16010705761	BMC fails to load during the first boot and after G3	Under Investigation
16010743708	BMC sensor log shows Critical errors for "CPU 1 & CPU2 " VR during the OOB SPS upgrade/downgrade	Under Investigation
2209851140	Node Manager limiting is not properly restored after seamless update (SUSRAM being cleared)	Under Investigation
2209880557	OpenBMC Redfish always report BIOS version 00.00 on seamless systems	Under Investigation

Parameter	Definition
No fix	There are no plans to fix this issue.
Fixed	This known issue has been previously fixed.
WA	There is a workaround available for this issue.
Rejected	Investigation identified not a defect or not reproducible.

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Item	System
Baseboard	Wilson City-ICX
Mem type	DDR4
CPU	QTDL (2S)
OS	Fedora 31
Boot Mode	UEFI
Memory Speed	2666
DDR4 quantity (32 max 2S)	1*8 GB DIMM 1Rx8 PC4

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		Ingredient	SW Version Details
Firmware		CPLD	0x03
		IFWI	2020.15.2.02.1507_0014.D32
		BMC	WhitleyOBMC-wht-0.53-0-gc84022-a6da583
		SPS Capsule	29_44_Single_Dual
		BIOS Capsule	14D31_14D32
		Microcode Capsule	R0_0x30_0x31
Fedora	OS	Fedora	31
Windows	Utilities	SPS Tools	4.2.97.185

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IFWI: 2020.15.2.02.1507_0014.D32	
Ingredient	Version
BIOSID	0014.D32
MCU	m_97_606a0_80000031
SPS	E5_04.04.03.044.0
BIOSACM	Debug_NT,v0.8.6_LBG
SINIT	Debug_NT,v0.8.6_LBG

