



Ice Lake ES2 Platform Intel® IOM Firmware

Release Notes - NDA

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Revision History

Revision Number	Description	Revision Date
02.005.0.00	Initial release	June 2018
02.008.00	Release	Aug 2018
02.009.00	Release	Aug 2018
02.00A.0.00	Release	Oct 2018
02.00B.0.00	ES2 Alpha BKC Release ww01'19	Jan 2019
02.00C.0.00	ES2 Beta BKC Release ww04'19	Jan 2019



1 Introduction

1.1 Scope of Document

This document provides component level details of the downloaded kit and the contents of each folder in the kit.

1.2 Acronyms

Term	Description
BIOS	Basic Input Output System
FW	Firmware

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2 *Issue Status Definitions*

This document provides sightings and bugs report for Intel® CSME Firmware 13.0 SKU for the Ice Lake Platform. At the time of a milestone release, this report will be distributed with the Intel® CSME Kit and will provide information on new issues and the status of old issues (replacing the Release Notes document).

Closed Issues: This category will only display closed issues within the current Intel® CSME Kit release. After each release, old issues will be dropped down to the “Archive” section and then new closed issues will take its place back up top for the next release. If an issue is posted in this section, it will indicate that the issue has been verified and fixed within the kit that is being released.

Known Issues: This category will display all Known Issues since the Alpha release and will remain in this section until fixed or noted otherwise. “Known Issues” are still under investigation and may or may not be root caused.

Archive – Fixes in Previous Kits: This category will display all closed issues that were closed in their respected kit#. This section will serve as a history of fixed issues.

Sightings listed in this document apply to the Ice Lake SKU’s unless noted otherwise in this document or in the sightings tracking systems.

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3 Intel® IOM New Features - RCR's

RCR Number	Change Information
N/A	N/A

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4 Closed Issues

Issue Number	Description	Details	Fixed in Version
1305791215	Thunderbolt may cause PKG-C state demotion	Thunderbolt internal registers save/restore issue may cause PKG-C get demotion to PKG-C3 after a few iterations of enter/exit from PKG-C10.	02.008.00
1407392783	Hang may occur while processor in PKG-C10	Hang may occur while Thunderbolt Dissconnect/connect when the processor is in PKG-C10	02.008.00
1407357281	HPD issues may occur	Hot plug detect issues may occur due to TCSS registers may not saved/restored while VNN goes off	02.008.00
1305829383	HPD issues may occur while connect/disconnect external display.	A third External display may not rcognized on port1 while two Thunderbolt display connected through Thunderbolt hub to port0 at the same time. Recognition issue may occur while disconnecting display from Thunderbolt hub and display already connected to port1 in parallel (or alternately)	02.009.00
1407572536	Sporadic Hang may occur while processor enters S3 state when USB device is connected	Under certain condition XHCI controller may fail to enter S3 state request and may cause the system hang.	02.009.00
1407968446	Sporadic Hang may occur while processor enters to S3/S4 state when connecting TBT device	Under certain conditions the XHCI controller may fail to response to register read command and may cause system hang.	02.00A.0.00
1806446221	Cat Error may occur after disconnecting Thunderbolt device.	After disconnect a TBT device, Thunderbolt may not answer PM_REQ when Power management fully enabled.	02.00A.0.00
1407631148	Two display panel connected to a single thunderbolt port may not resume correctly after sleep/wake state.	Connecting display panel to certain port in sleep state when two display panel where connected to single thunderbolt port will not resume correctly, the two display panels may not appear.	02.00A.0.00
2205995978	Sporadic CAT Error may occur while device not attached.	CAT Error may occur when entering PKG-C10 while no device connected due to race condition in IOM controller	02.00B.0.00

Closed Issues

Issue Number	Description	Details	Fixed in Version
1408790507	Sporadic CATERR may occur while the processor enters an Sx state when PCIe and Thunderbolt are enabled	While Thunderbolt enables clock gating, PCIe may sporadically fail to respond Sx entry requests from IOM and may cause a system hang.	02.00C.0.00



5 Open / Known Issues – To Date

Issue Number	Description	Details	Fix status
1306178049	CATERR may be observed during Thunderbolt device plug/unplug event with enabled Thunderbolt RTD3 and TCcold	CATERR may be observed while connecting a Type-C device when the Thunderbolt Controller is in TC Cold state	Plan fix in IOM FW

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6 Archive

6.1 Fixed in Previous Kits

N/A

6.2 RCR's

N/A

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